職類介紹_類比/射頻IC設計
類比

會什麼	
學校修過科目	• 主修類比/混合式電路設計 • 熟悉 Serdes/PLL/PMIC/ADC/DAC/PGA/Filter 等analog/mixed-signal電路設計 • 熟悉 Memory/ IO/ ESD 設計 • 主修電磁學 • 熟悉高速信號 SI/PI 或微波天線及被動元件設計

相關專長要求	
Serdes	• 熟悉 CDR, PLL, synthesizer, high speed TX/RX者尤佳
PMIC	• 熟悉電源管理電路 LDO, Buck, Boost, Buck-Boost, Charger... 等電路
Converters	• 熟悉Data converter, filter, variable gain amplifier and audio amplifier 熟悉Ethernet, bio-medical analog front-end or analog AI processor者尤佳
Foundation IP	• 熟悉記憶體電路 low power, low voltage and high speed SRAM, ROM, TCAM and CIM 電路 熟悉IO電路, 特殊應用IO者尤佳 熟悉ESD/LUP防護設計, 有EOS/Surge/System ESD 經驗尤佳 有半導體元件, VLSI design, LowPower/HighSpeed 數位電路設計 相關經驗
SI/PI	• 主修電磁學, 熟悉高速信號 SI/PI 或微波天線及被動元件設計

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職類介紹_類比/射頻IC設計
射頻

主修射頻相關者:

The candidate will design, supervise layout, and help characterize transceiver circuits used in latest cellular / WiFi and Bluetooth products. Circuits may include but not limited to: receiver frontend, transmitter frontend, baseband circuitry (filters / ADC / DAC), synthesizers (PLL), crystal oscillators (XO), and LO generation and distribution.

The ideal candidate shall have an excellent track record in RFIC design and verification.

- MS or PhD degree in Electrical and Electronics Engineering or equivalent.
- Strong analytical skill is important for this position.
- Hands-on transceiver building block design experience.
- Solid understanding of highly scaled CMOS process and device characteristics, and scaling-related impact to circuit design.
- EM knowledge and EM tool usage are plus.

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